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APPLICATION NO.	FI	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/812,894	(03/31/2004	Stephen H. Tang	INTEL-0056	INTEL-0056 4982	
34610	7590	01/12/2006		EXAMINER		
FLESHNER & KIM, LLP P.O. BOX 221200				PHAN, TRONG Q		
CHANTILLY, VA 20153				ART UNIT	PAPER NUMBER	

DATE MAILED: 01/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

			1				
	Application No.	Applicant(s)					
Advisory Action	10/812,894	TANG ET AL.					
Before the Filing of an Appeal Brief	Examiner	Art Unit					
	TRONG PHAN	2827					
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence add	ress				
THE REPLY FILED 128/25 FAILS TO PLACE THIS APPLICAT	THE REPLY FILED 128/25 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.						
1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:							
a) The period for reply expires 3 months from the mailing date of the final rejection.							
b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.							
Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).							
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL							
2. The Notice of Appeal was filed on A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).							
AMENDMENTS							
3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because (a) They raise new issues that would require further consideration and/or search (see NOTE below); (b) They raise the issue of new matter (see NOTE below);							
(c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for							
appeal; and/or (d)☐ They present additional claims without canceling a	corresponding number of finally re	jected claims.					
NOTE: See Continuation Sheet. (See 37 CFR 1.116 and 41.33(a)).							
4. The amendments are not in compliance with 37 CFR 1.		ompliant Amendmen	t (PTOL-324).				
 5. Applicant's reply has overcome the following rejection(s 6. Newly proposed or amended claim(s) would be a 		timely filed amends	nent canceling				
 Newly proposed or amended claim(s) would be a the non-allowable claim(s). 	mowable il subl'ilited ili a separate	, umely med amendin	ient canceing				
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is pro The status of the claim(s) is (or will be) as follows: Claim(s) allowed:		vill be entered and an	explanation of				
Claim(s) objected to:							
Claim(s) rejected: <u>1-34</u> .							
Claim(s) withdrawn from consideration: AFFIDAVIT OR OTHER EVIDENCE							
8. The affidavit or other evidence filed after a final action, b because applicant failed to provide a showing of good ar and was not earlier presented. See 37 CFR 1.116(e).	ut before or on the date of filing a l nd sufficient reasons why the affida	Notice of Appeal will vit or other evidence	not be entered is necessary				
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to showing a good and sufficient reasons why it is necessal The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER 	overcome <u>all</u> rejections under appe ry and was not earlier presented. S	al and/or appellant fa See 37 CFR 41.33(d)	ails to provide a (1).				
11. The request for reconsideration has been considered by	ut does NOT place the application	in condition for allowa	ance because:				

TRONG PHAN
PRIMARY EXAMINER

13. Other: ____.

12. Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s).

Continuation Sheet (PTOL-303)

Application No.

Continuation of 3. NOTE: The feature "the supply voltage line to receive a first supply voltage based on a first mode of the memory device and to receive a second supply voltage based on a second mode of the memory device, the second supply voltage being different than the first supply voltage" as newly added to claim 1 raises new issue that would require further consideration and search. Regarding claims 9-34, Oliver, 4,567,577, does teach in Fig. 2 the teaching of using NMOS transistor 35 for providing a first supply voltage VSS to two transistors 25 and 27 of the SRAM cell based on a first STANBY/inactive mode and using NMOS transistor 36 for providing a second supply voltage VEE to two transistors 25 and 27 of the SRAM cell based on a second WRITE mode.